

Understanding Delta-Sigma Data Converters (IEEE Press Series on Microelectronic Systems)

Shanthi Pavan, Richard Schreier, Gabor C. Temes



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This new edition introduces operation and design techniques for Sigma-Delta converters in physical and conceptual terms, and includes chapters which explore developments in the field over the last decade

- Includes information on MASH architectures, digital-to-analog converter (DAC) mismatch and mismatch shaping
- Investigates new topics including continuous-time $\Delta\Sigma$ analog-to-digital converters (ADCs) principles and designs, circuit design for both continuous-time and discrete-time $\Delta\Sigma$ ADCs, decimation and interpolation filters, and incremental ADCs
- Provides emphasis on practical design issues for industry professionals

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